佳恩半导体 JIAENSEMI

Datasheet

JNCF120R075HR1

1200V 75mΩ SiC MOSFET

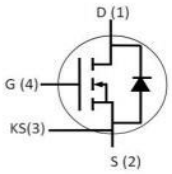
V_{DS}	=	1200V
I_D@25°C	=	45A
$R_{DS(on)}$	=	75mΩ

Features

- High blocking voltage with low On-resistance
- Fast switching with low capacitances
- Intrinsic diode with low reverse recovery (Qrr)

Package

D S K G



Inner circuit

RoHS COMPLIANT

HF

Applications

- PV Inverters
- Charging Piles
- Energy storage systems
- Industrial power supply
- Industrial Motors

Marking

JNCF120R075HR1	=	Product number
RXXXX HXXX	=	Wafer-batch Packging-batch

Maximum Ratings @Tc=25°C (unless otherwise specified)

Parameter	Symbol	Test conditions	Values	Unit
Drain-Source Voltage	V_{DSmax}	V_{GS}=0V, I_D=100μA	1200	V
Gate-Source Voltage (static)	V_{GSop}	Static	-5/+20	V
Continuous Drain Current	I_D	V_{GS}=20V, T_c=25°C	45	A
		V_{GS}=20V, T_c=100°C	33	
Pulsed Drain Current	$I_{D(pulse)}$	Pulse width t_p limited by T_{jmax}	70	A
Power Dissipation	P_D	T_C=25°C, T_j=175°C	288	W
Operating Junction Range	T_j		-55 to +175	°C
Storage Temperature Range	T_{stg}		-55 to +175	°C

Electrical Characteristics @Tc=25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS}=0V, I_D=100μA	1200	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=5mA	-	2.8	-	V
		V_{DS}=V_{GS}, I_D=5mA, T_j=175°C	-	1.9	-	
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=1200V, V_{GS}=0V	-	1	-	μA
Gate-Source Leakage Current	I_{GSS}	V_{GS}=20V, V_{DS}=0V	-	10	-	nA
Drain-Source On-State Resistance	$R_{DS(on)}$	V_{GS}=20V, I_D=20A	-	75	-	mΩ
		V_{GS}=20V, I_D=20A, T_j=175°C	-	133	-	
		V_{GS}=18V, I_D=20A	-	82	-	
		V_{GS}=18V, I_D=20A, T_j=175°C	-	137	-	
Transconductance	g_{fs}	V_{DS}=20V, I_{DS}=20A	-	10	-	S
		V_{DS}=20V, I_{DS}=20A, T_j=175°C	-	11	-	
Turn-On Switching Energy (Body Diode FWD)	E_{ON}	V_{DS}=800V, V_{GS}=-5V/20V, I_D=20A, $R_{G(ext)}$=2.5Ω, L=200μH, T_j=25°C FWD=NC1M120C75GT	-	399	-	μJ
Turn-Off Switching Energy (Body Diode FWD)	E_{OFF}		-	36	-	
Turn-On Delay Time	$t_{d(on)}$	V_{DD}=800V, V_{GS}=-5V/20V, I_D=20A, $R_{G(ext)}$=2.5Ω, L=200μH	-	22	-	ns
Rise Time	t_r		-	18	-	
Turn-Off Delay Time	$t_{d(off)}$		-	27	-	
Fall Time	t_f		-	10	-	
Gate to Source Charge	Q_{gs}	V_{DS}=800V, V_{GS}=-5V/20V, I_D=20A	-	35	-	nC
Gate to Drain Charge	Q_{gd}		-	25	-	
Total Gate Charge	Q_g		-	87	-	
Input Capacitance	C_{iss}	V_{GS}=0V, V_{DS}=1000V f=1MHz V_{AC}=25mV	-	1450	-	pF
Output Capacitance	C_{oss}		-	66	-	
Reverse Transfer Capacitance	C_{rss}		-	13	-	
C_{OSS} Stored Energy	E_{oss}		-	40	-	μJ
Internal Gate Resistance	$R_{G(int)}$	f=1MHz, V_{AC}=25mV	-	2.4	-	Ω

Reverse Diode Characteristics @Tc=25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Diode Forward Voltage	V_{SD}	V_{GS}=-5V, I_{SD}=10A	-	4.9	-	V
		V_{GS}=-5V, I_{SD}=10A, T_j=175°C	-	4.0	-	V
Continuous Diode Forward Current	I_S	V_{GS}=-5V	-	46	-	A
Reverse Recovery time	t_{rr}	V_{GS}=-5V, I_{SD}=20A, V_R=800V, di/dt=3000A/μs	-	12	-	ns
Reverse Recovery Charge	Q_{rr}		-	75	-	nC
Peak Reverse Recovery Current	I_{rrm}		-	10	-	A

Thermal Characteristics

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Thermal Resistance from Junction to Case	$R_{\theta JC}$		-	0.52	-	°C/W

Typical Performance

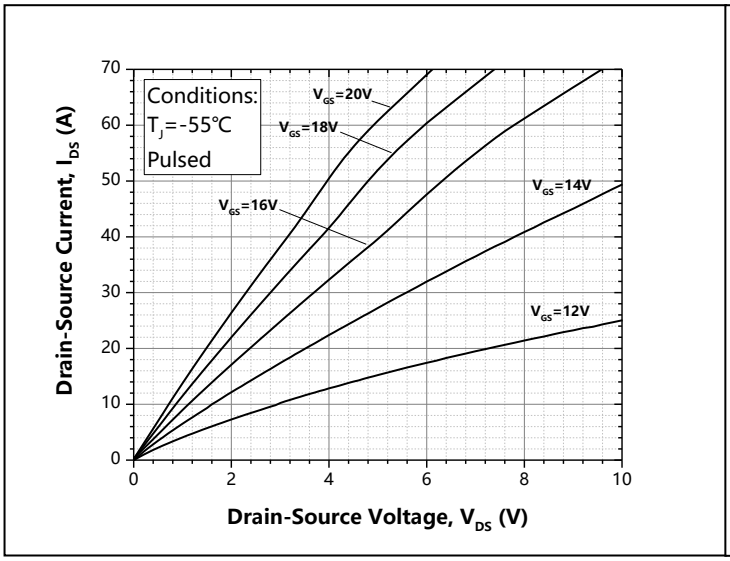


Figure 1. Output Characteristics T_J=-55°C

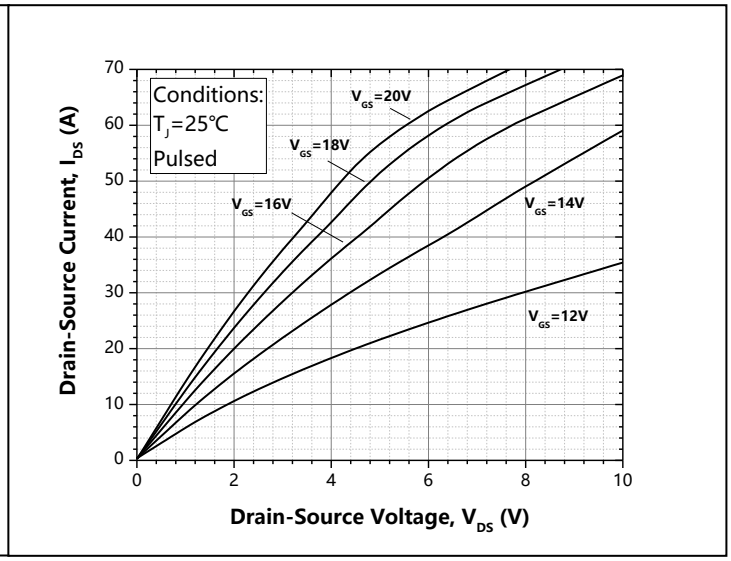


Figure 2. Output Characteristics T_J=25°C

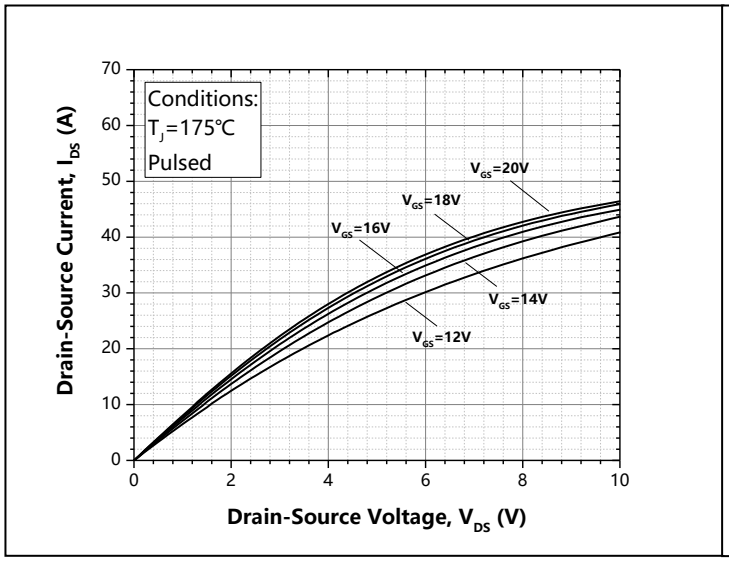


Figure 3. Output Characteristics T_j=175°C

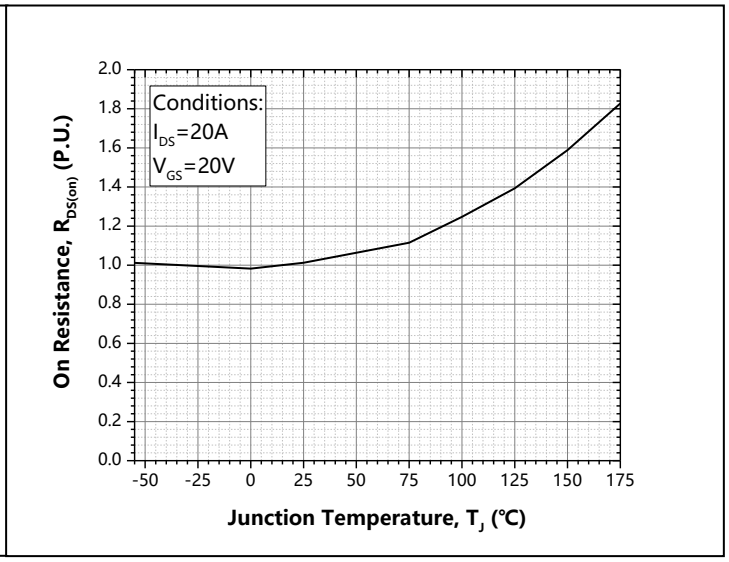


Figure 4. Normalized On-Resistance vs. Temperature

Typical Performance

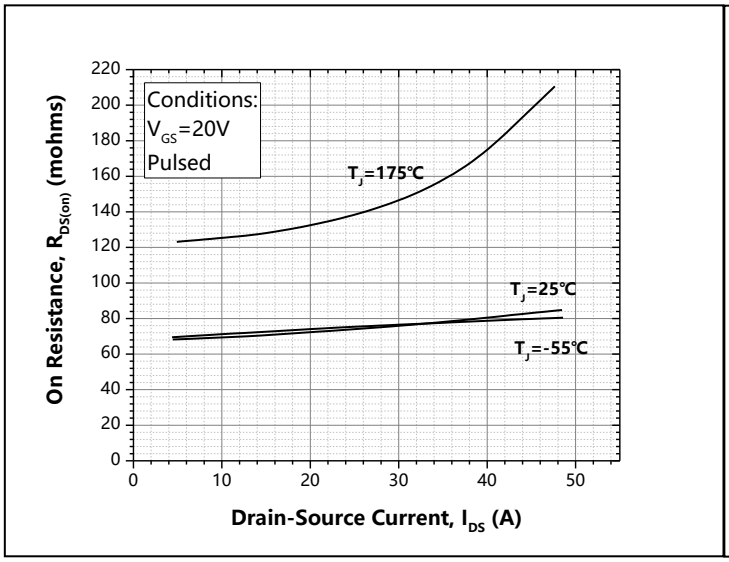


Figure 5. On-Resistance vs. Drain Current For Various Temperatures

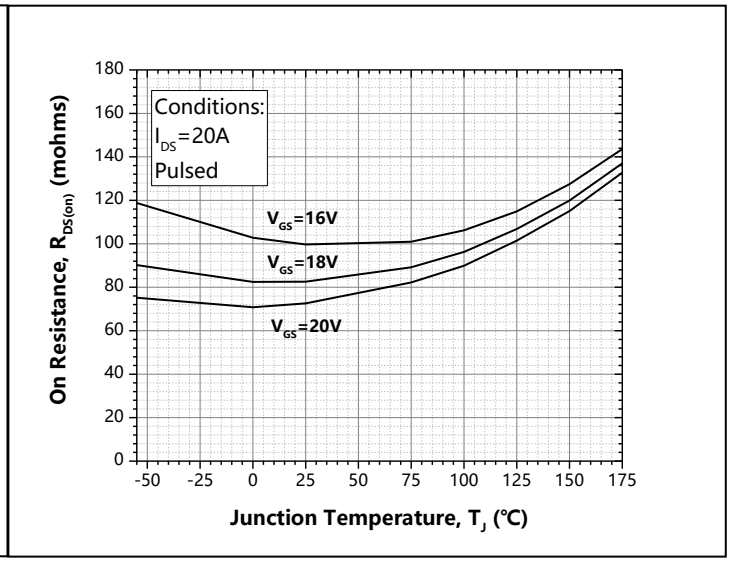


Figure 6. On-Resistance vs. Temperature For Various Gate Voltage

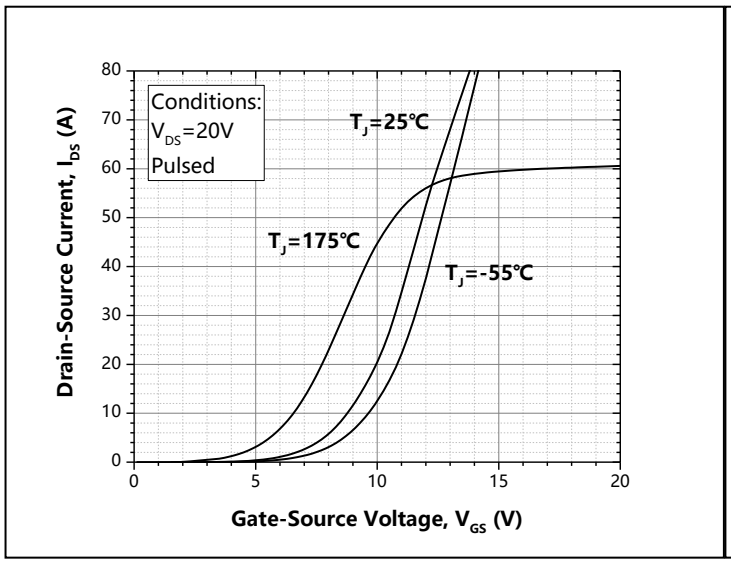


Figure 7. Transfer Characteristic for Various Junction Temperatures

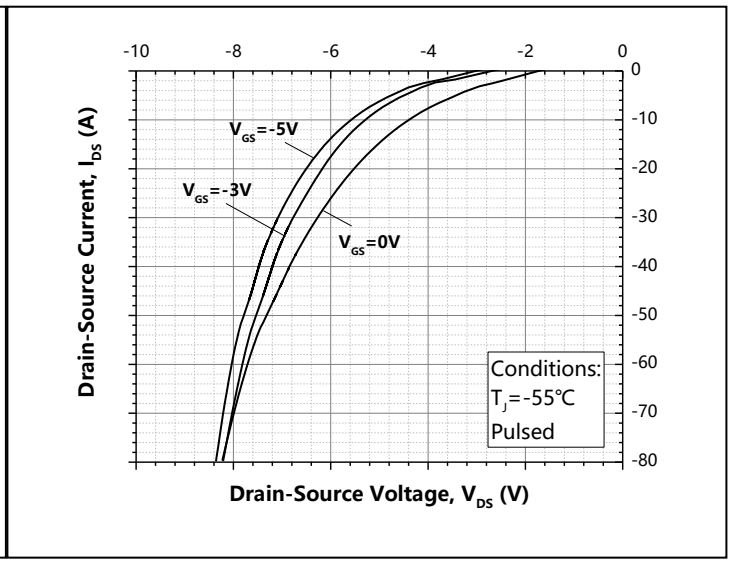


Figure 8. Body Diode Characteristic at -55°C

Typical Performance

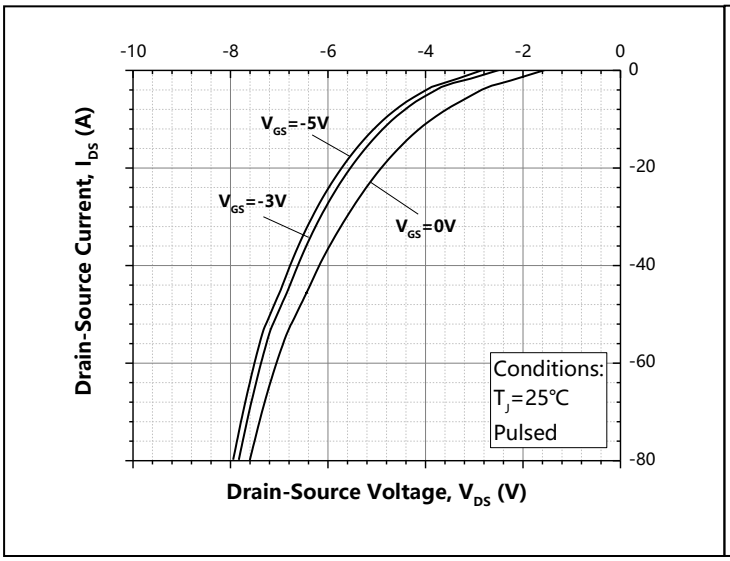


Figure 9. Body Diode Characteristic at 25°C

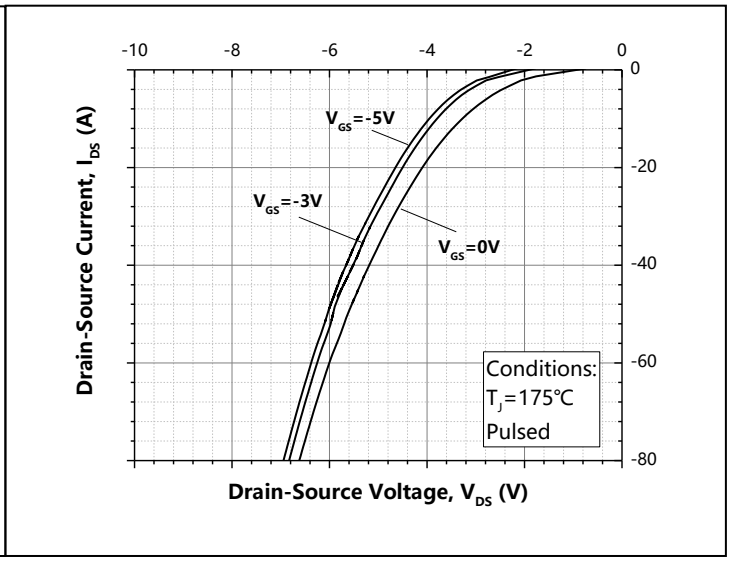


Figure 10. Body Diode Characteristic at 175°C

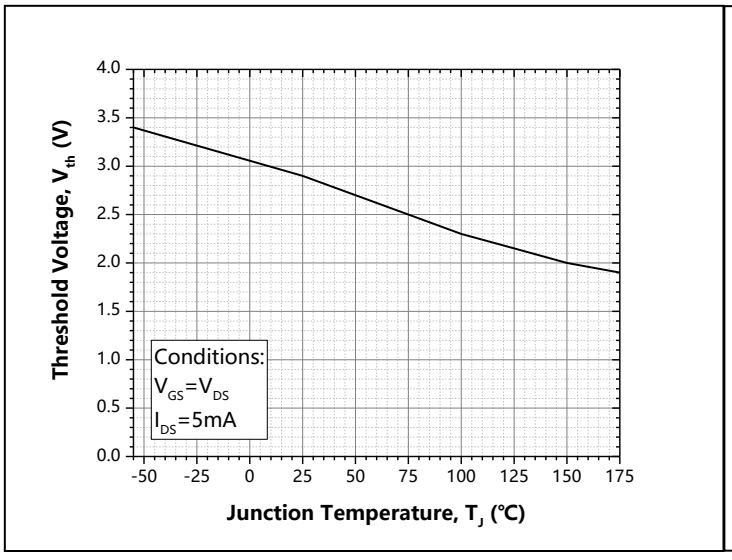


Figure 11. Threshold Voltage vs. Temperature

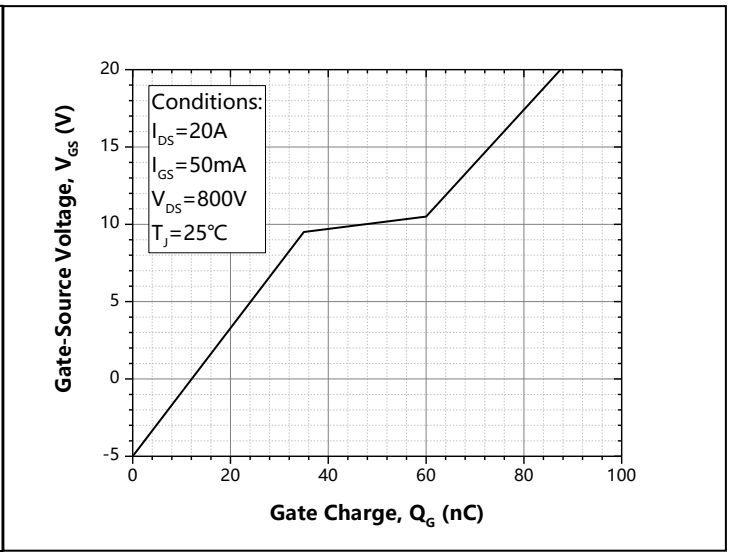


Figure 12. Gate Charge Characteristics

Typical Performance

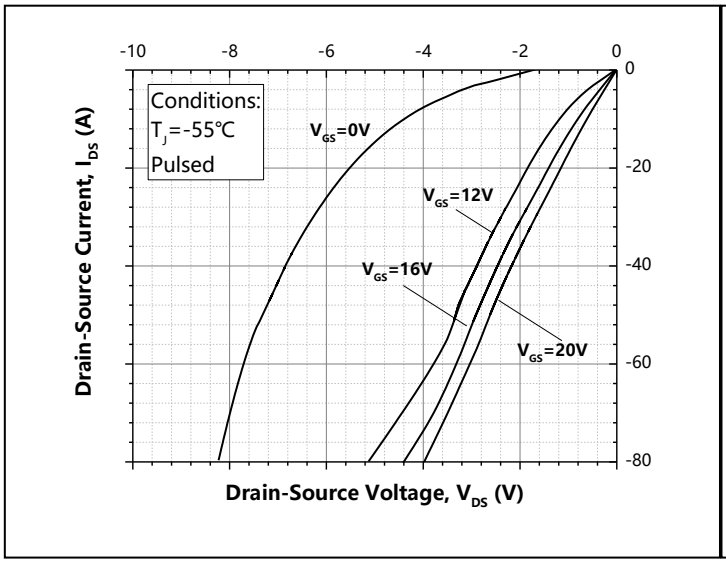


Figure 13. 3rd Quadrant Characteristic at -55°C

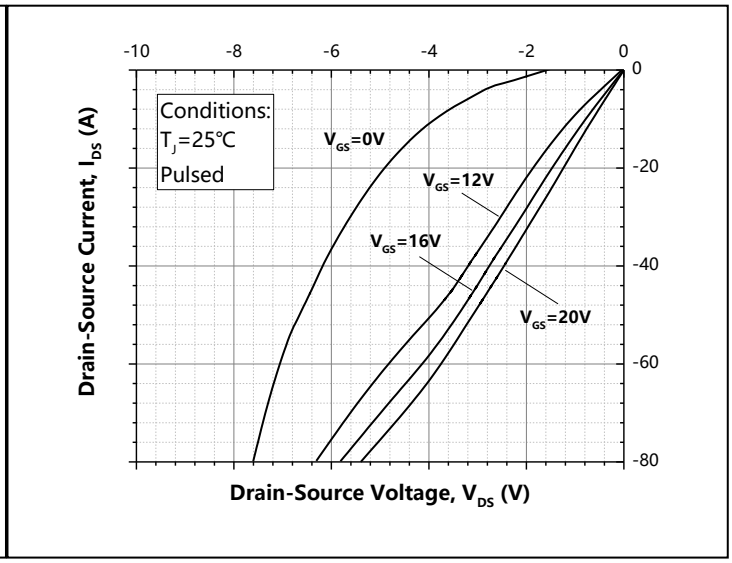


Figure 14. 3rd Quadrant Characteristic at 25°C

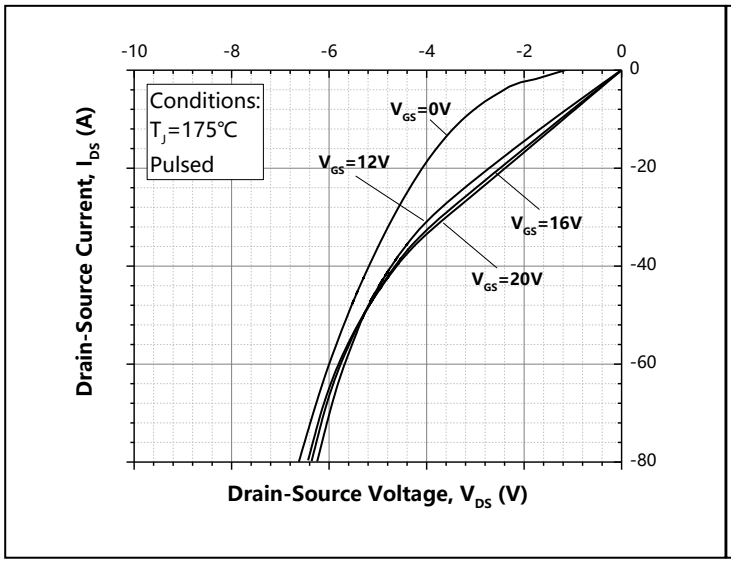


Figure 15. 3rd Quadrant Characteristic at 175°C

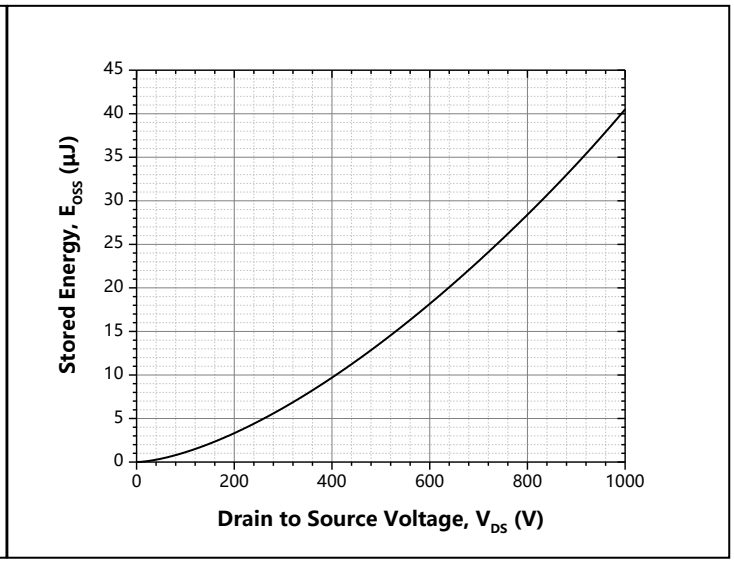


Figure 16. Output Capacitor Stored Energy

Typical Performance

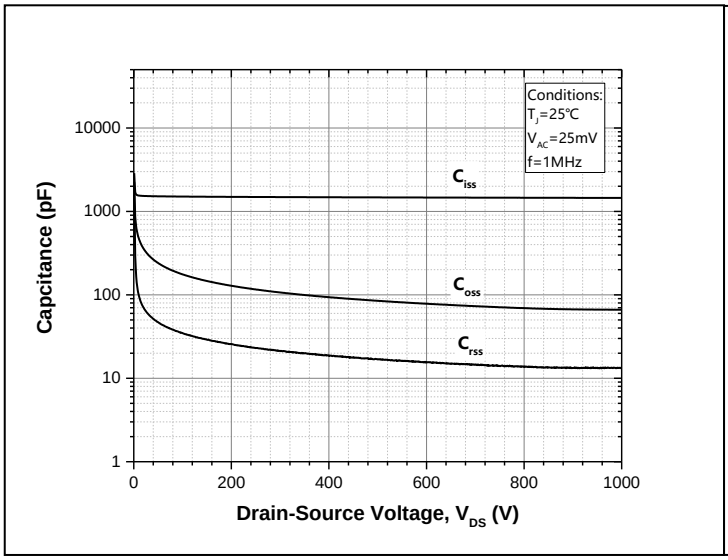


Figure 17. Capacitances vs. Drain-Source Voltage

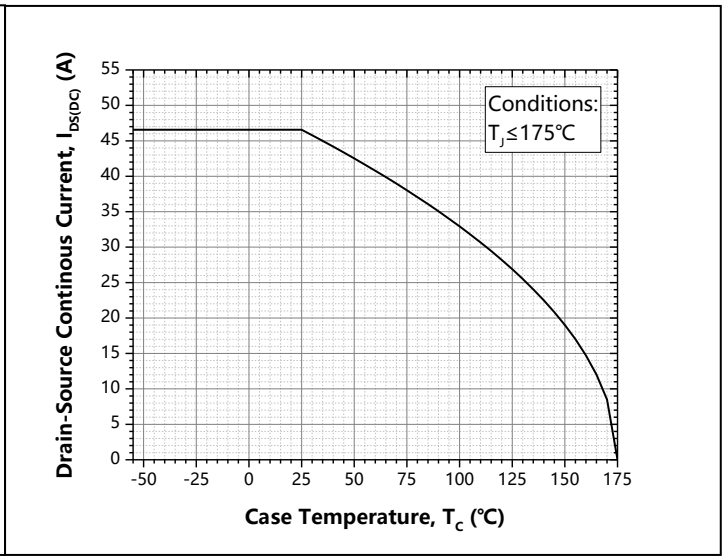


Figure 18. Continuous Drain Current Derating vs. Case Temperature

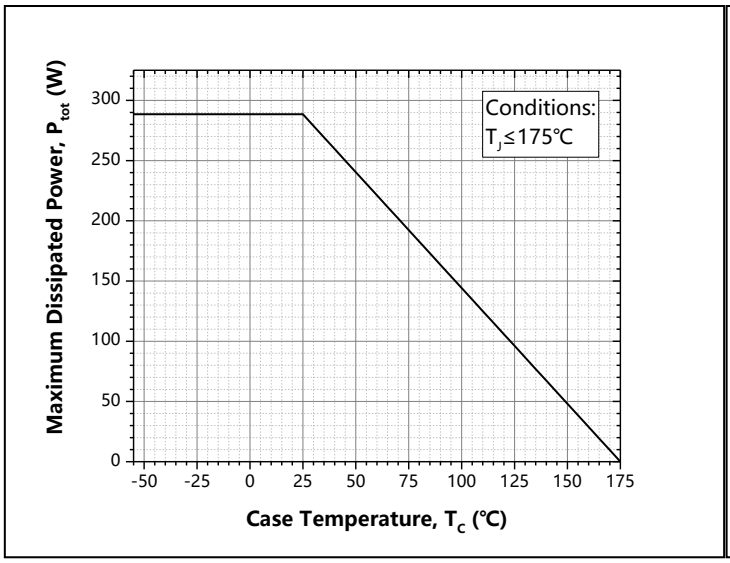


Figure 19. Maximum Power Dissipation Derating vs. Case Temperature

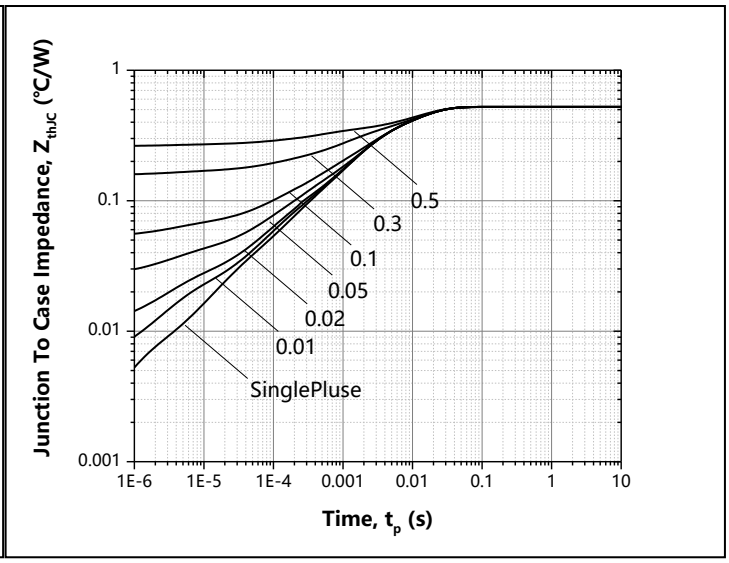


Figure 20. Transient Thermal Impedance (Junction - Case)

Typical Performance

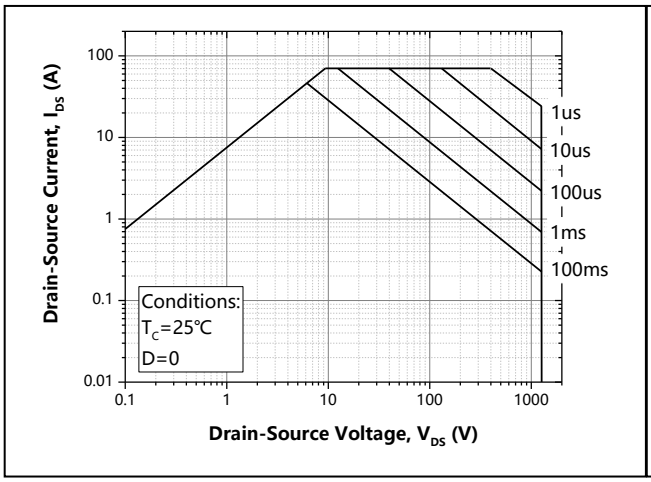


Figure 21. Safe Operating Area

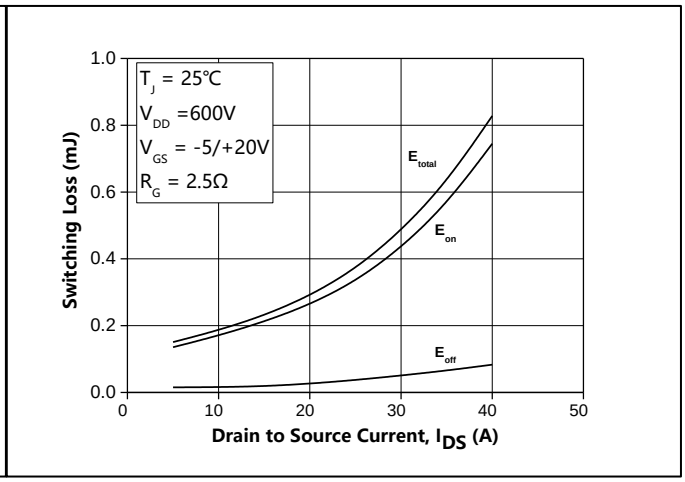


Figure 22. Clamped Inductive Switching Energy vs. Drain Current (V_{DD}=600V)

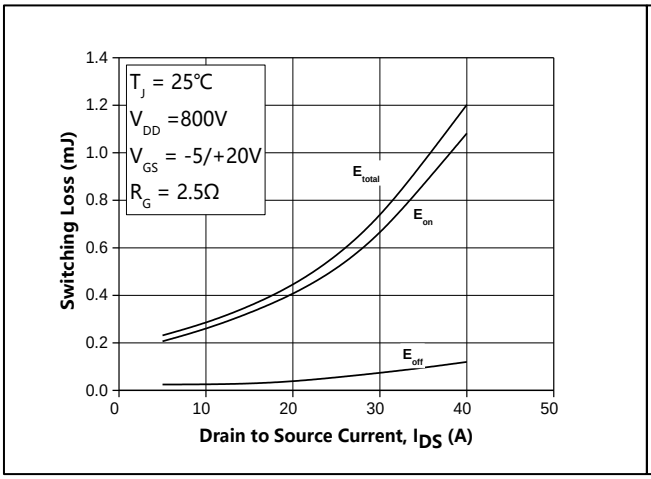


Figure 23. Clamped Inductive Switching Energy vs. Drain Current (V_{DD}=800V)

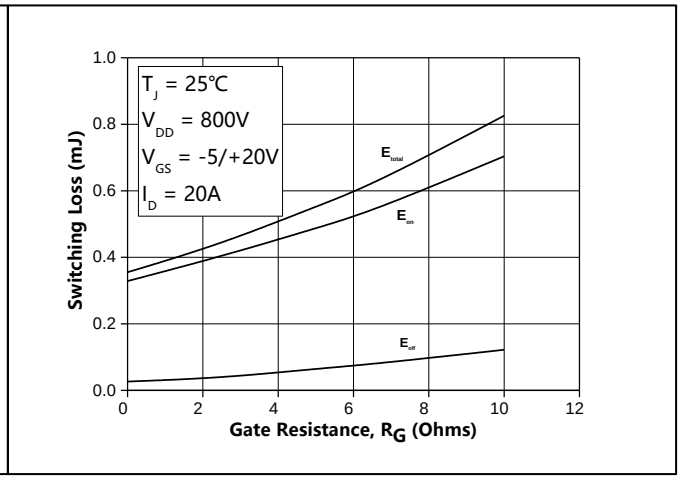


Figure 24. Clamped Inductive Switching Energy vs. $R_{G(ext)}$

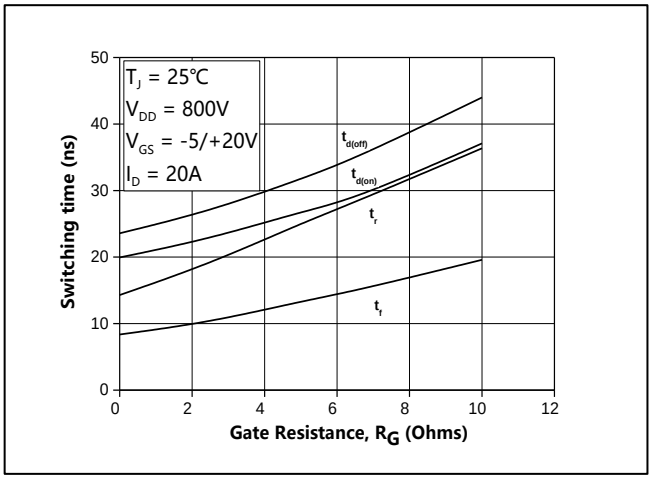
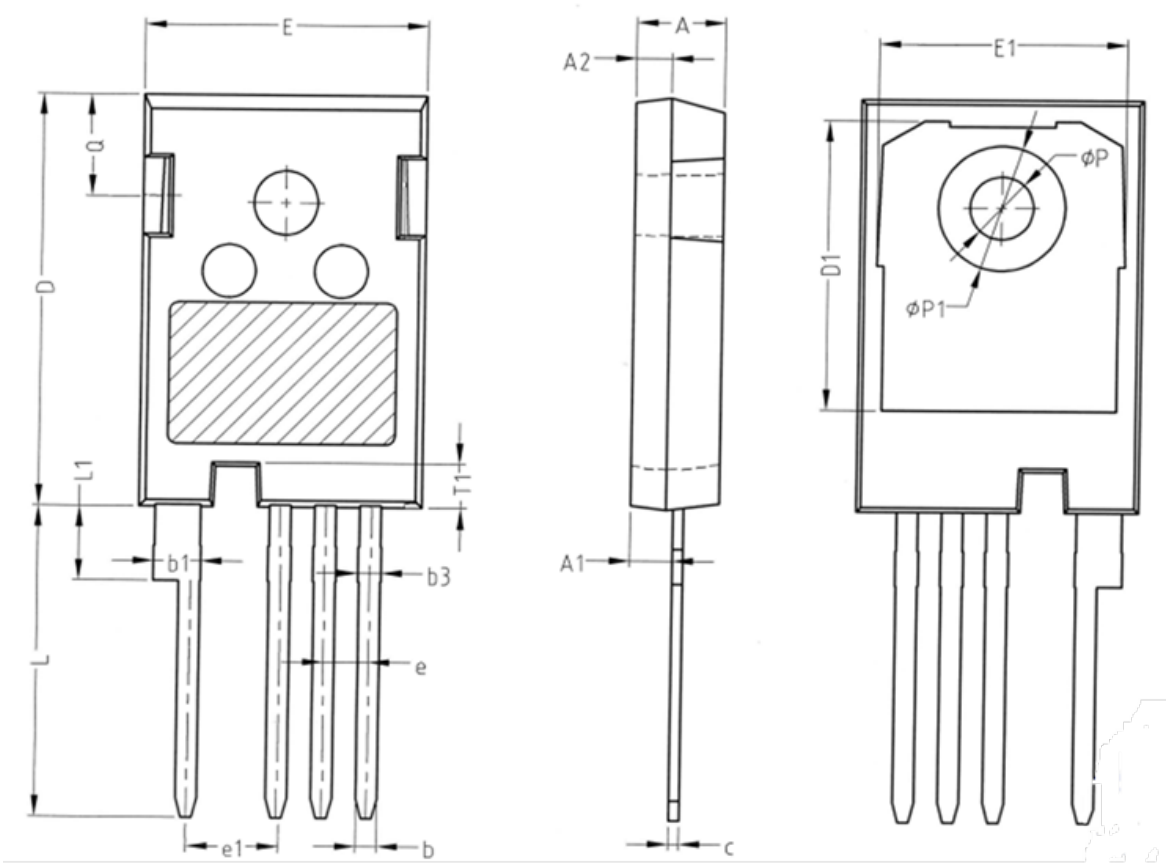


Figure 25. Switching Time vs. $R_{G(ext)}$

Package Outline: TO-247-4L



COMMON DIMENSIONS

SYMBOL	MM		
	MIN	NOM	MAX
A	4.80	5.00	5.20
A1	2.21	2.41	2.61
A2	1.80	2.00	2.20
b	1.06	1.21	1.36
b1	2.33	2.63	2.93
b3	1.07	1.30	1.60
c	0.51	0.61	0.75
D	23.30	23.45	23.60
D1	16.25	16.55	16.85
E	15.74	15.94	16.14
E1	13.72	14.02	14.32
T1	2.35	2.50	2.65
e	2.54 BSC		
e1	5.08 BSC		
Q	5.49	5.79	6.09
L	17.27	17.57	17.87
L1	3.99	4.19	4.39
Φp	3.40	3.60	3.80
Φp1	7.19 REF		

NOTE:
1. ALL DIMENSIONS ARE LISTED IN MILLIMETERS, ANGLES ARE IN DEGREES.
2. ALL METAL SURFACES ARE TIN PLATED (MATTE), EXCEPT AREA OF CUT.

Product Ordering Information

Order Number	Packing Type
JNCF120R075HR1	Tube

Revision History

Revision	Date	Subjects (major changes since last revision)
1.0	6 June. 2024	Official first release