

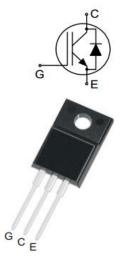
IGBT

Features

- 650V,25A
- V_{CE(sat)(typ.)}=2.1V@V_{GE}=15V,I_C=25A
- High speed switching
- Higher system efficiency
- Soft current turn-off waveforms
- Square RBSOA

General Description

JIAEN Trench IGBTs offer lower losses and higher energy efficiency for application such as Motor control, general inverter and other soft switching applications.



JNG25T65FS1

Absolute Maximum Ratings

Symbol	Parameter	Value	Units
VCES	Collector-Emitter Voltage	650	V
V _{GES}	Gate-Emitter Voltage	<u>+</u> 30	V
	Continuous Collector Current (Tc=25 °C)	50	А
lc	Continuous Collector Current (T _c =100 $^{\circ}$ C)	25	А
Ісм	Pulsed Collector Current (Note 1)	75	А
IF	Diode Continuous Forward Current ($T_C=100$ °C)	25	А
Іғм	Diode Maximum Forward Current (Note 1)	75	А
t _{sc}	Short Circuit Withstand Time	10	us
PD	Maximum Power Dissipation (T _c =25 $^{\circ}$ C)	41.7	W
	Maximum Power Dissipation ($T_{C}\text{=}100^{\circ}\text{C}\text{)}$	16.7	W
TJ	Operating Junction Temperature Range	-55 to +150	°C
Tstg	Storage Temperature Range	-55 to +150	°C

Thermal Characteristics

Symbol	Parameter	Max.	Units
R _{th j-c}	Thermal Resistance, Junction to case for IGBT	3.0	°C/W
Rth j-c	Thermal Resistance, Junction to case for Diode	2.9	°C/W
Rth j-a	Thermal Resistance, Junction to Ambient	62.5	°C/ W



Electrical Characteristics (Tc=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
BV _{CES}	Collector-Emitter Breakdown Voltage	V _{GE} = 0V, I _C = 250uA	650	-	-	V
I _{CES}	Collector-Emitter Leakage Current	V _{CE} = 650V, V _{GE} = 0V	-	-	100	uA
I _{GES}	Gate Leakage Current, Forward	V_{GE} =±20V, V_{CE} = 0V	-	-	±100	nA
V _{GE(th)}	Gate Threshold Voltage	$V_{GE} = V_{CE}, I_C = 250 \text{uA}$	5.1	-	6.9	V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	V _{GE} =15V, I _C = 25A	-	2.1	2.7	V
Qg	Total Gate Charge	V _{cc} =480V	-	31.2		nC
Qge	Gate-Emitter Charge	V _{GE} =15V	-	7.7		nC
Qgc	Gate-Collector Charge	Ic=25A	-	13.3		nC
t d(on)	Turn-on Delay Time		-	22	-	ns
t r	Turn-on Rise Time	V_{cc} =400V V_{GE} =15V I_{c} =25A R_{G} =15 Ω Inductive Load	-	44	-	ns
t d(off)	Turn-off Delay Time		-	75	-	ns
t f	Turn-off Fall Time		-	88	-	ns
Eon	Turn-on Switching Loss		-	0.66	-	mJ
Eoff	Turn-off Switching Loss	T _C =25 ℃	-	0.49	-	mJ
Ets	Total Switching Loss		-	1.15	-	mJ
Cies	Input Capacitance	V _{CE} =25V	-	978	-	pF
Coes	Output Capacitance	V _{GE} =0V	-	90	-	pF
Cres	Reverse Transfer Capacitance	f = 1MHz	-	8	-	pF

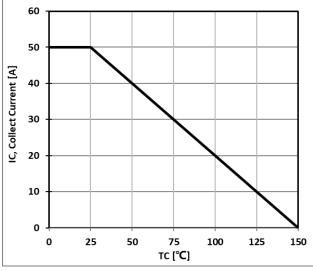
Electrical Characteristics of Diode (Tc=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
V _F	Diode Forward Voltage	I _F =25A	-	1.65	3.0	V
trr	Diode Reverse Recovery Time	V _{CE} = 400V	-	60		ns
l r r	Diode peak Reverse Recovery Current	I _F = 25A	-	15.6		А
Qr r	Diode Reverse Recovery Charge	Rg=15 Ω	-	518		nC

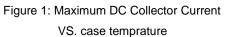
Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature





Typical Performance Characteristics



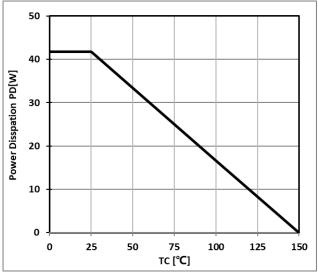
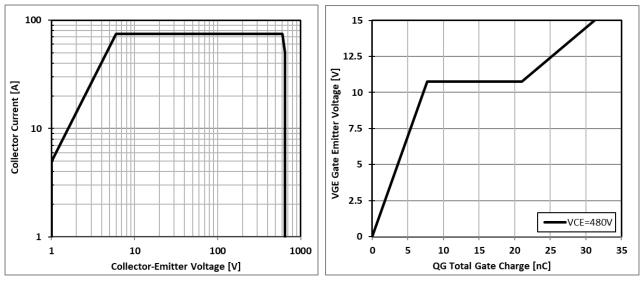


Figure 2: Power Dissipation VS. Case Temperature



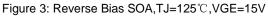
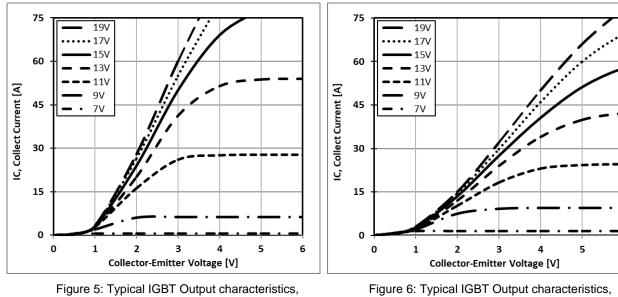


Figure 4: Typical Gate charge VS. VGE,IC=25A



6



TC=25°C;tp=300us

TC=150℃;tp=300us

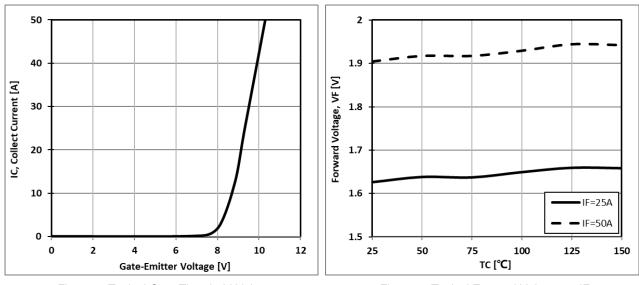
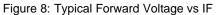
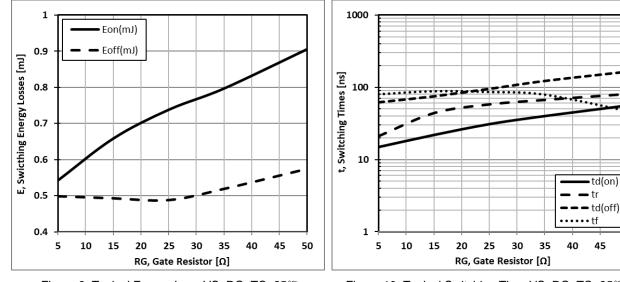


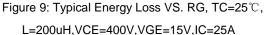
Figure 7: Typical Gate Threshold Voltage

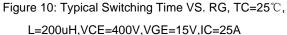


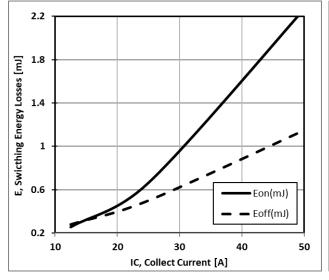


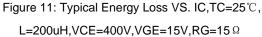
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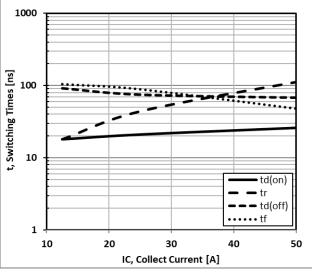


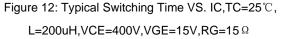




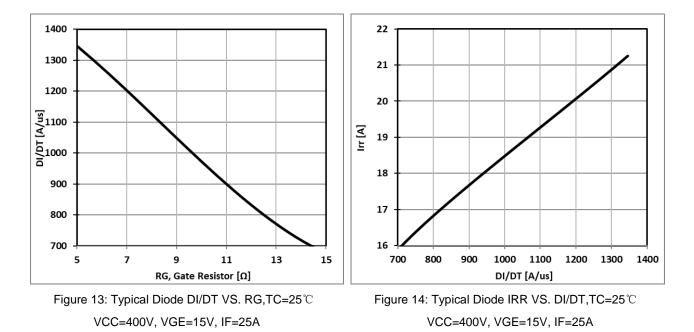


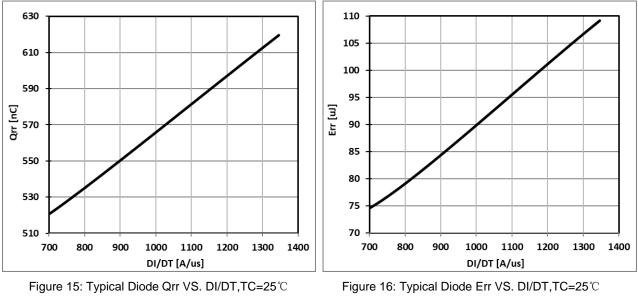




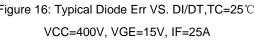




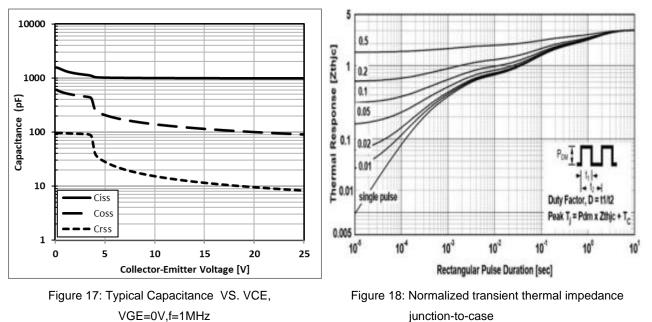








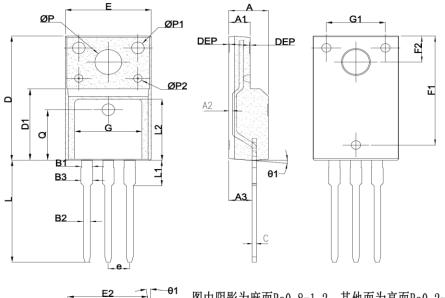




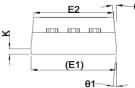
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TO-220F PACKAGE OUTLINE



COMMON DIMENSIONS				
SYMBOL		mm		
	MIN	NOM	MAX	
*A	4.50	4.70	4.90	
*A1	2.34	2.54	2.74	
*A2	0.38	0.43	0.48	
*A3	2.66	2.76	2.86	
B1	1.23	1.28	1.33	
*B2	0.75	0.80	0.85	
*B3	1.28	-	1.43	
*C	0.45	0.50	0.60	
*[)	15.67	15.87	16.07	
*D1	9.04	9.12	9.20	
*e	2.49	2.54	2.59	
*E	10.00	10.16	10.32	
E1	9.94	10.04	10.14	
E2	9.36	9.46	9.56	
F1	13.80	13.90	14.00	
*F2	3.20	3.30	3.40	
G	7.80	8.00	8.20	
G1	6.90	7.00	7.10	
K	0.65	0.70	0.75	
*L	12.78	12.98	13.18	
*L1	3.13	3.23	3.33	
L2	7.70	7.80	7.90	
Q	6. 5REF			
*ΦP	3.08	-	3.48	
ΦP1	1.40	1.50	1.60	
φ P2	0.95	1.00	1.05	
* θ1	3°	5°	7°	
DEP 0.05 0.10			0.15	
带*为检验尺寸				



图中阴影为麻面Ra0.8-1.2,其他面为亮面Ra0.2-0.4



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