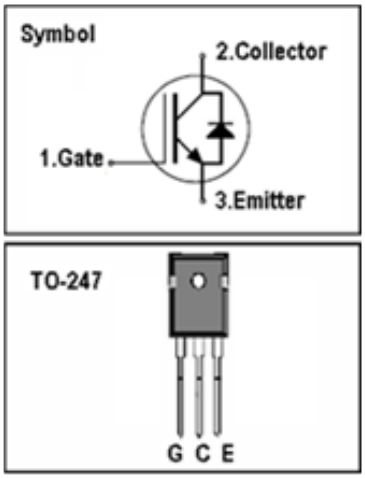
IGBT



Features

- 650V,15A
- $V_{CE(sat)(typ.)}$=1.9V@V_{GE}=15V,I_C=15A
- High speed switching
- Higher system efficiency
- Soft current turn-off waveforms
- Square RBSOA

General Description

JIAEN Trench IGBTs offer lower losses and higher energy efficiency for application such as Motor control, general inverter and other soft switching applications.

Absolute Maximum Ratings

Symbol	Parameter	Value	Units
V_{CES}	Collector-Emitter Voltage	650	V
V_{GES}	Gate-Emitter Voltage	±30	V
I_C	Continuous Collector Current (T_C=25 ℃)	30	A
	Continuous Collector Current (T_C=100℃)	15	A
I_{CM}	Pulsed Collector Current (Note 1)	45	A
I_F	Diode Continuous Forward Current (T_C=100 ℃)	15	A
I_{FM}	Diode Maximum Forward Current (Note 1)	45	A
t_{sc}	Short Circuit Withstand Time	10	us
P_D	Maximum Power Dissipation (T_C=25 ℃)	104	W
	Maximum Power Dissipation (T_C=100℃)	41.7	W
T_J	Operating Junction Temperature Range	-55 to +150	℃
T_{STG}	Storage Temperature Range	-55 to +150	℃

Thermal Characteristics

Symbol	Parameter	Max.	Units
$R_{th\ j\text{-}c}$	Thermal Resistance, Junction to case for IGBT	1.2	℃/ W
$R_{th\ j\text{-}c}$	Thermal Resistance, Junction to case for Diode	1.8	℃/ W
$R_{th\ j\text{-}a}$	Thermal Resistance, Junction to Ambient	40	℃/ W

Electrical Characteristics (T_C=25℃ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{CES}	Collector-Emitter Breakdown Voltage	V_{GE}= 0V, I_C= 250uA	650	-	-	V
I_{CES}	Collector-Emitter Leakage Current	V_{CE}= 650V, V_{GE}= 0V	-	-	100	uA
I_{GES}	Gate Leakage Current, Forward	V_{GE}=±20V, V_{CE}= 0V	-	-	±100	nA
$V_{GE(th)}$	Gate Threshold Voltage	V_{GE}= V_{CE}, I_C= 250uA	4.5	-	6.5	V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	V_{GE}=15V, I_C= 15A	-	1.9	2.5	V
Q_g	Total Gate Charge	V_{CC}=480V V_{GE}=15V I_C=15A	-	40.7		nC
Q_{ge}	Gate-Emitter Charge		-	4.19		nC
Q_{gc}	Gate-Collector Charge		-	30.7		nC
$t_{d(on)}$	Turn-on Delay Time	V_{CC}=400V V_{GE}=15V I_C=15A R_G=15Ω Inductive Load T_C=25 ℃	-	16	-	ns
t_r	Turn-on Rise Time		-	20	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	94	-	ns
t_f	Turn-off Fall Time		-	118	-	ns
Eon	Turn-on Switching Loss		-	0.31	-	mJ
Eoff	Turn-off Switching Loss		-	0.32	-	mJ
Ets	Total Switching Loss		-	0.63	-	mJ
C_{ies}	Input Capacitance	V_{CE}=25V V_{GE}=0V f = 1MHz	-	629	-	pF
C_{oes}	Output Capacitance		-	45	-	pF
C_{res}	Reverse Transfer Capacitance		-	7	-	pF

Electrical Characteristics of Diode (T_C=25℃ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_F	Diode Forward Voltage	I_F=15A	-	1.55	3.0	V
t_{rr}	Diode Reverse Recovery Time	V_{CE} = 400V I_F= 15A Rg=15Ω	-	120		ns
I_{rr}	Diode peak Reverse Recovery Current		-	17.5		A
Q_{rr}	Diode Reverse Recovery Charge		-	690		nC

Notes：

1. Repetitive Rating: Pulse width limited by maximum junction temperature

Typical Performance Characteristics

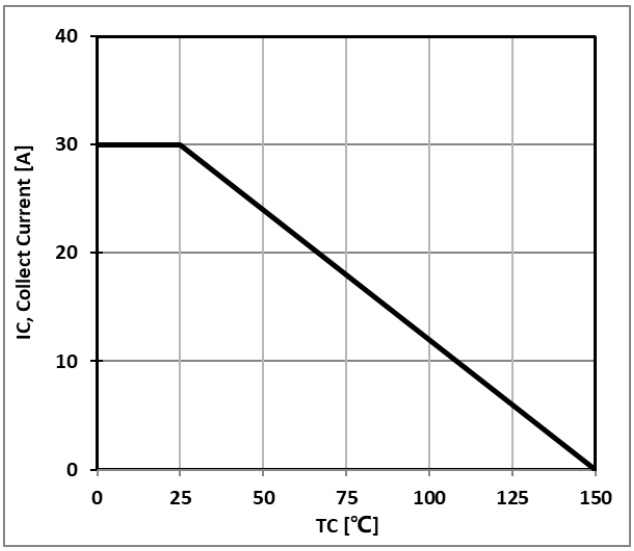


Figure 1: Maximum DC Collector Current VS. case temprature

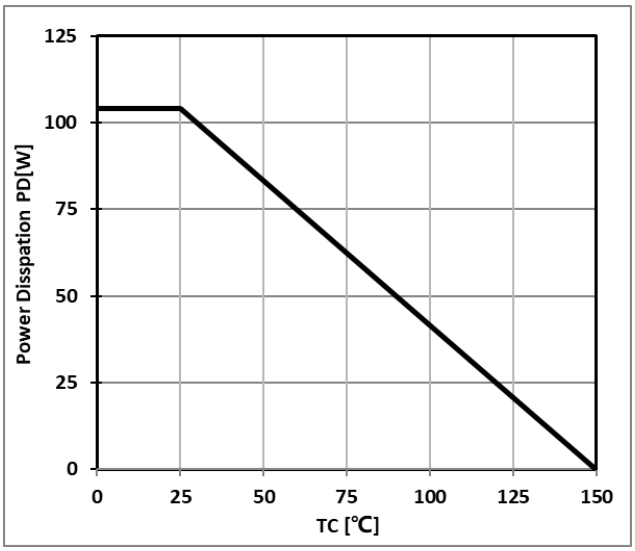


Figure 2: Power Dissipation VS. Case Temperature

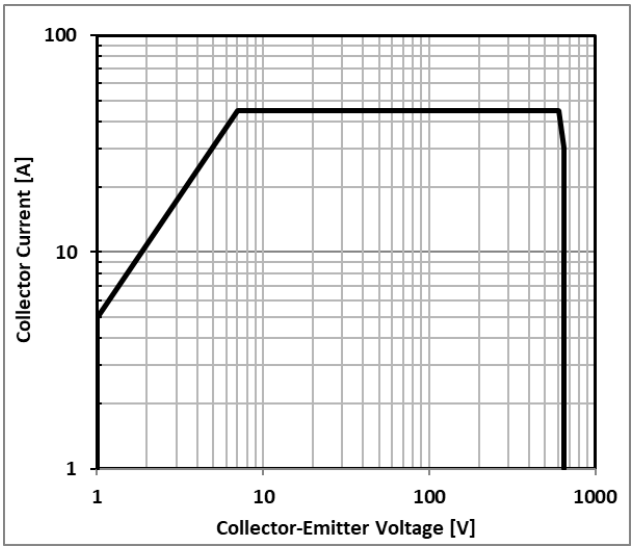


Figure 3: Reverse Bias SOA,TJ=125℃,VGE=15V

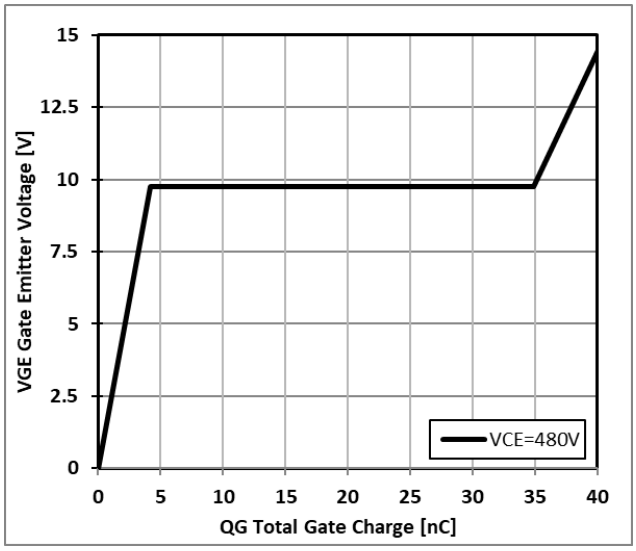


Figure 4: Typical Gate charge VS. VGE,IC=15A

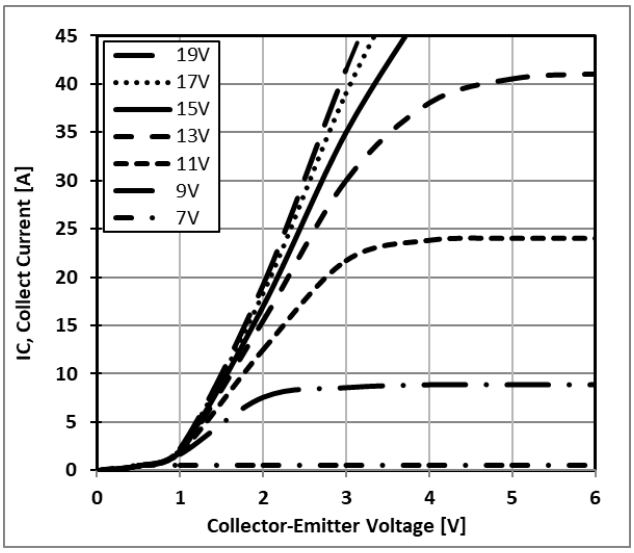


Figure 5: Typical IGBT Output characteristics, TC=25℃;tp=300us

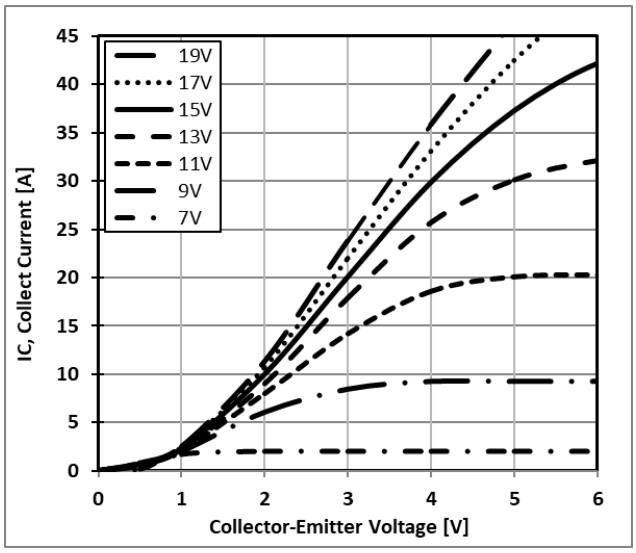


Figure 6: Typical IGBT Output characteristics, TC=150℃;tp=300us

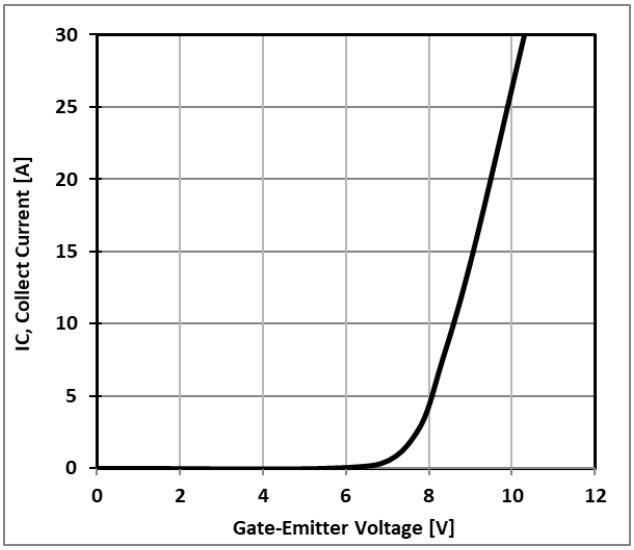


Figure 7: Typical Gate Threshold Voltage

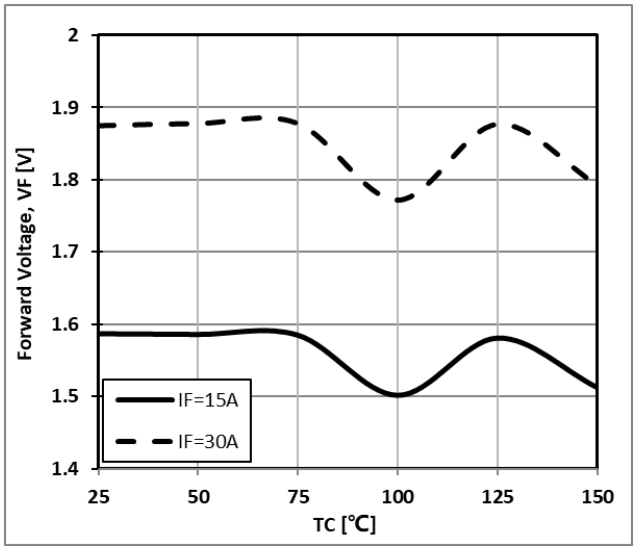


Figure 8: Typical Forward Voltage vs IF

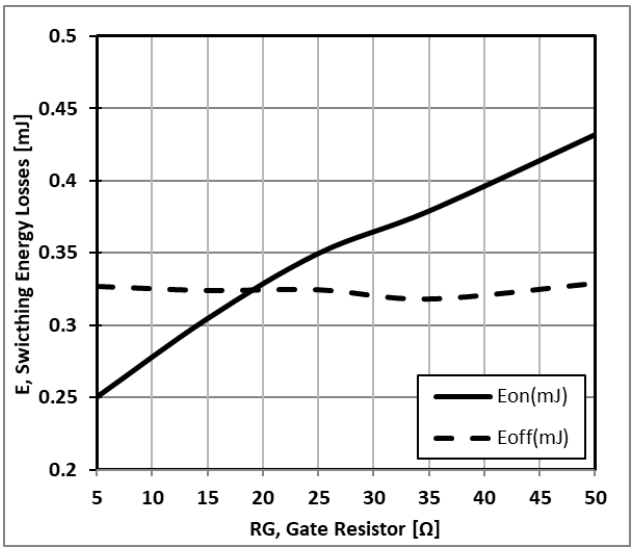


Figure 9: Typical Energy Loss VS. RG, TC=25℃, L=200uH,VCE=400V,VGE=15V,IC=15A

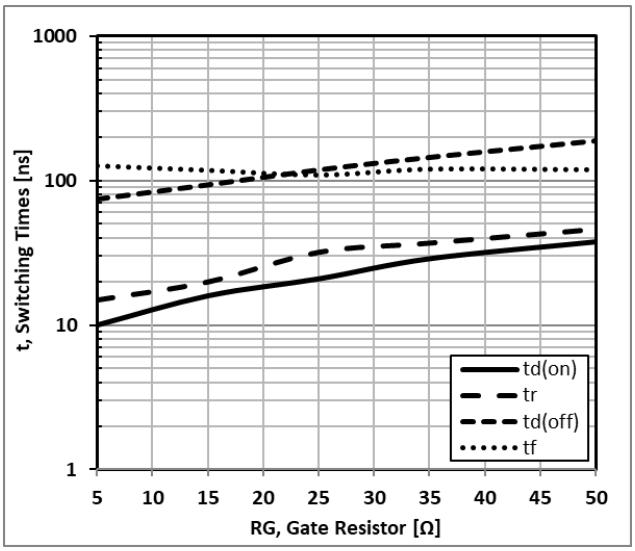


Figure 10: Typical Switching Time VS. RG, TC=25℃, L=200uH,VCE=400V,VGE=15V,IC=15A

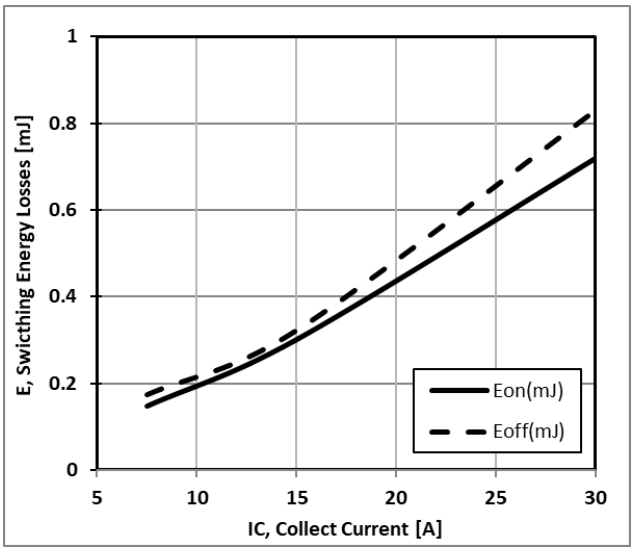


Figure 11: Typical Energy Loss VS. IC,TC=25℃, L=200uH,VCE=400V,VGE=15V,RG=15Ω

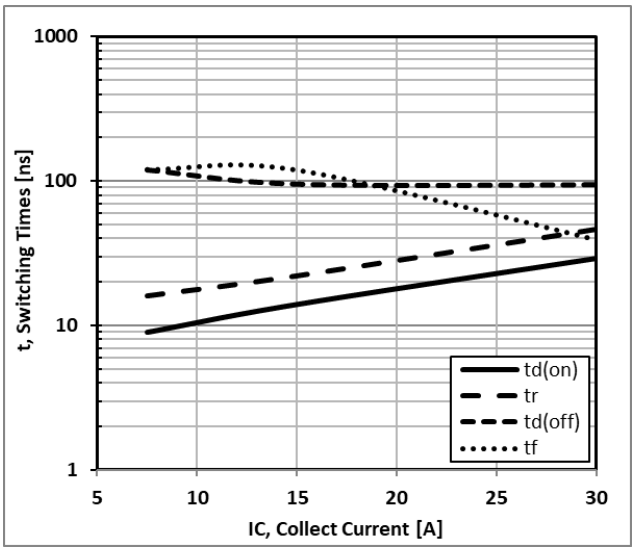


Figure 12: Typical Switching Time VS. IC,TC=25℃, L=200uH,VCE=400V,VGE=15V,RG=15Ω

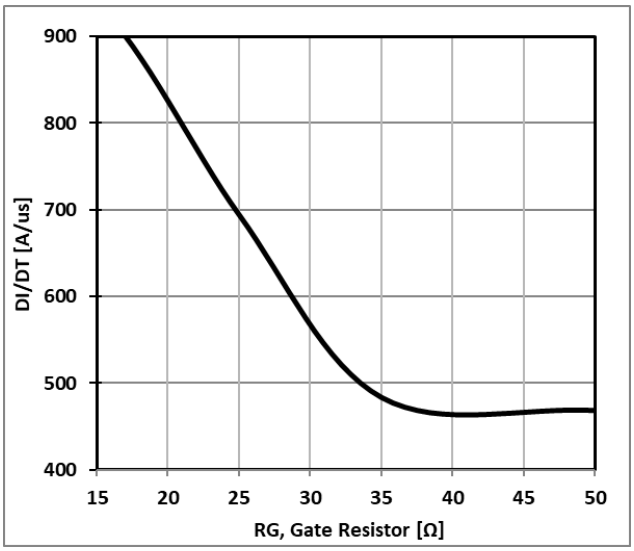


Figure 13: Typical Diode DI/DT VS. RG,TC=25℃
VCC=400V, VGE=15V, IF=15A

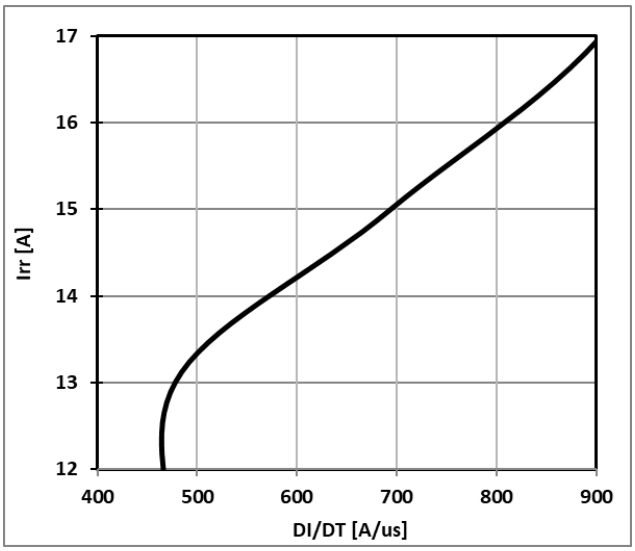


Figure 14: Typical Diode IRR VS. DI/DT,TC=25℃
VCC=400V, VGE=15V, IF=15A

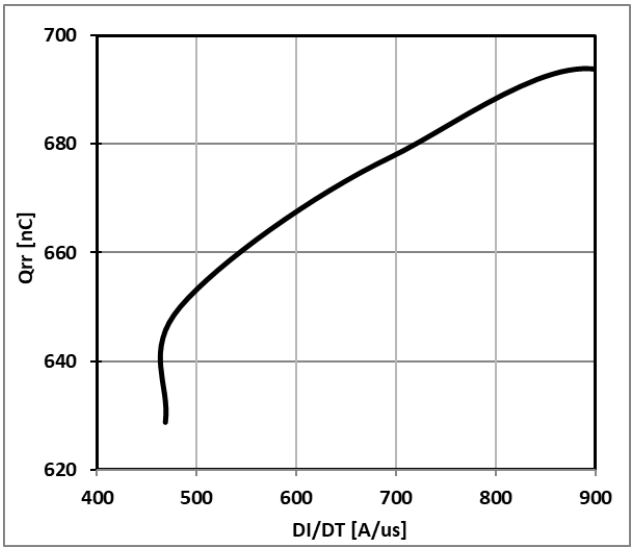


Figure 15: Typical Diode Qrr VS. DI/DT,TC=25℃
VCC=400V, VGE=15V, IF=15A

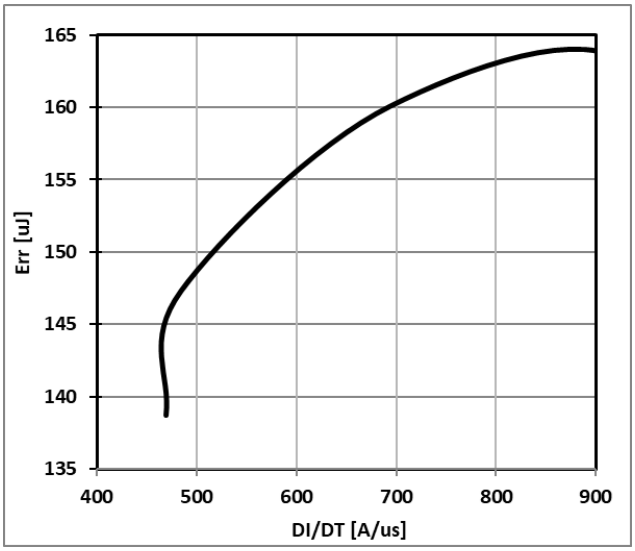


Figure 16: Typical Diode Err VS. DI/DT,TC=25℃
VCC=400V, VGE=15V, IF=15A

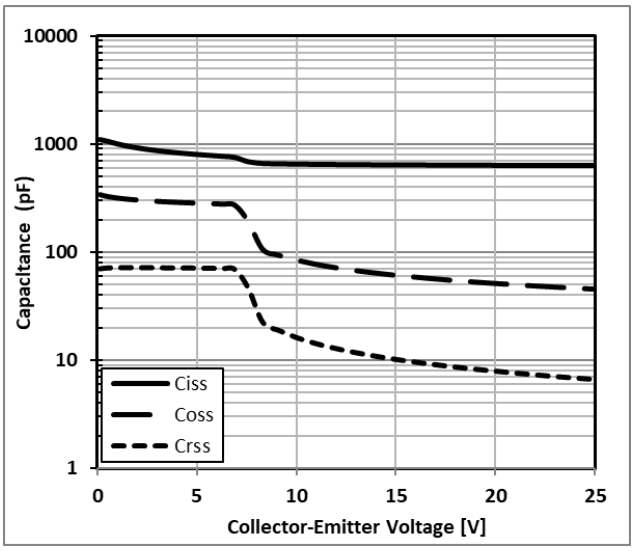


Figure 17: Typical Capacitance VS. VCE, VGE=0V,f=1MHz

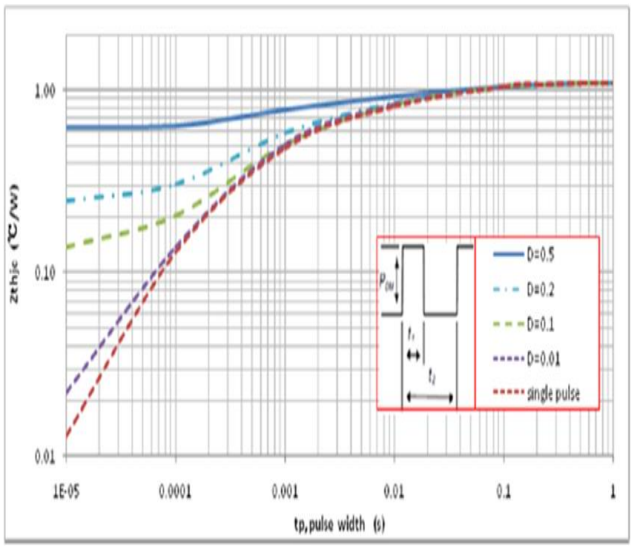
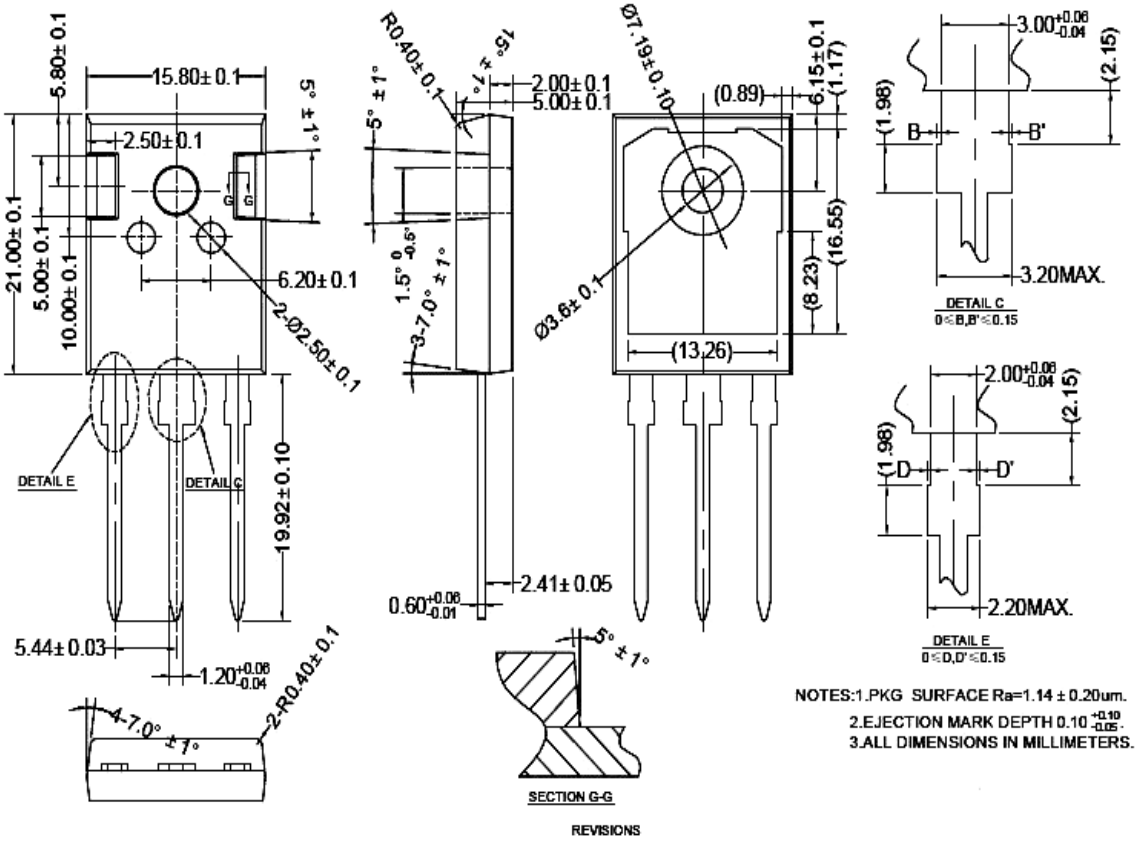


Figure 18: Normalized transient thermal impedance junction-to-case

TO-247 PACKAGE OUTLINE



公差标注	公差值	表面粗糙度
0	±0.2	Ra3.2~6.3
0.0	±0.1	Ra1.6~3.2
0.00	±0.01	Ra0.8~1.6
0.000	±0.005	Ra0.4~0.8
0.0000	±0.002	Ra0.2~0.4

0≤D,D'≤0.15

NOTES:1.PKG SURFACE Ra=1.14 ± 0.20um.
2.EJECTION MARK DEPTH 0.10 +0.10 -0.05.
3.ALL DIMENSIONS IN MILLIMETERS.

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